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Rectenna Design for K Band Application

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Abstract—An ultra-compact (2.5 cm²) rectenna design based on an innovative topology using a coplanar stripline connected to a crossed dipoles antenna array is reported here. Experimental results in the K-band show that a DC power up to 0.9 mW can be harvested on a 0.3 kΩ load by using the proposed compact rectenna for incident power density of 1413 μW/cm². The targeted application for this rectenna is to power autonomous wireless sensors for satellite health monitoring.

Keywords— Rectenna, energy harvesting, satellite application

I. INTRODUCTION

Recently, the topics of energy harvesting for satellite health monitoring was explored [1]-[3]. The idea consists of harvesting the spill-over loss of microwave antenna (operating in C, X, Ku, K or Ka bands) of broadcasting satellites by using appropriate rectenna designs. This harvested DC power available over the satellite lifetime can be used to power autonomous wireless sensors for satellite health monitoring. Rectenna topologies working on frequencies beyond 10 GHz were addressed for microwave power transmission [4]-[6], 24 GHz ISM band applications [7]-[9] or more recently satellite health monitoring [1]-[3]. This paper addresses an innovative rectenna topology for K band application. The design methodology, simulation and experimental results are presented in order to underline the performances of the proposed design.

II. RECTENNA DESIGN

A. Topology

The proposed topology is based on the use of a high gain planar antenna directly connected with a high frequency rectifier using (only) one Schottky diode. Fig. 1 shows the top view of the rectenna while its bottom view is represented in Fig. 2. The antenna consists of an array of two planar crossed dipoles. The rectifier consists of (i) a Schottky diode mounted on the top side of the planar circuit board (PCB) and (ii) a lumped filtering capacitor of 1.5 pF and a load (see Fig.2). Thus, the low-pass filtering function is achieved by the coplanar strip supported structure mounted on the bottom side of the rectenna. The connection between the diode and the low-pass filter is performed by using two via holes. The matching between antenna and rectifier input impedances is achieved by properly controlling the mounting position of the Schottky diode.

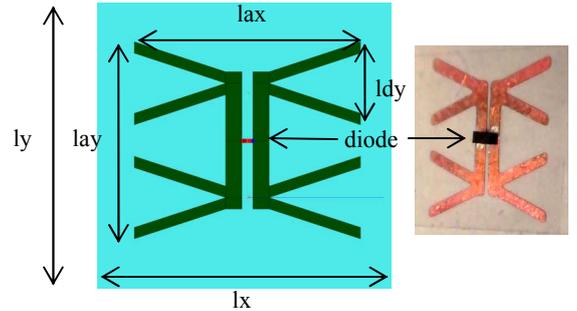


Fig. 1. Top view (left, not to scale) of the simulation model (FEKO) and a photo (top side) of the manufactured rectenna (right).

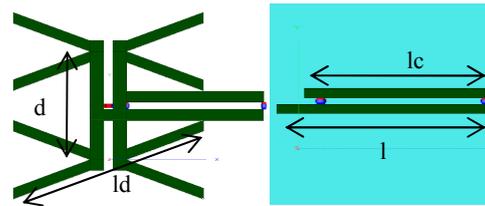


Fig. 2. Top view (left, not to scale) of the simulation model (PCB substrate is not represented) and bottom view (right, not to scale) of the rectenna.

Rectennas using coplanar stripline structures are reported in [2],[3] and [5] while microstrip supported topologies are reported in [2]-[3], [6]-[8]. A substrate integrated waveguide (SIW) rectenna is reported in [9].

B. Antenna design

The antenna located on the top side can be viewed as an array composed by 4 printed half-wavelength dipoles. This antenna is called here the *crossed dipoles array antenna* (CDAA). The maximum theoretical gain (G_{max} in dBi) of such an array antenna can be computed using the following formula:

$$G_{max}=10\cdot\log(4)+Gd=6+2.15=8.15\text{ dBi} \quad (1)$$

where $Gd=2.15\text{ dBi}$ is the gain of a lossless half-wavelength dipole. This gain is 3 dB higher when a metallic reflector is properly positioned below the antenna surface. Thus the expected maximum gain is 11.15 dBi. The main geometrical parameters for optimizing CDAA performances are: the length of the crossed dipole arms (l_d), the angle between crossed dipoles (α), the array step (d), the strip width (w) and the gap between the strips of the coplanar stripline (g). The length of

the crossed dipole arms (ld) and the array step must be close to the half-wavelength. The optimization procedure for CDAA is described in the next section. A standard 3D Cartesian coordinate system with the vertical Oz axis perpendicular to the rectenna/CDAA surface is chosen here. The diode was replaced by a voltage port while the capacitance (1.5 pF) and the load (e.g. 300 Ω) were modeled as port loads. By adopting this simulation model the impact on antenna performances of the overall rectifier layout (except the non-linear behavior of the diode) is estimated. Fig. 3 shows the simulated gain (by using FEKO software) in two vertical planes (xOz-plane: $\phi=0^\circ$ and yOz-plane: $\phi=90^\circ$) at 17.7 GHz. The half-power beamwidth is 60° . The inset of Fig. 3 shows the simulated 3D radiation pattern of the optimized CDAA at 17.7 GHz.

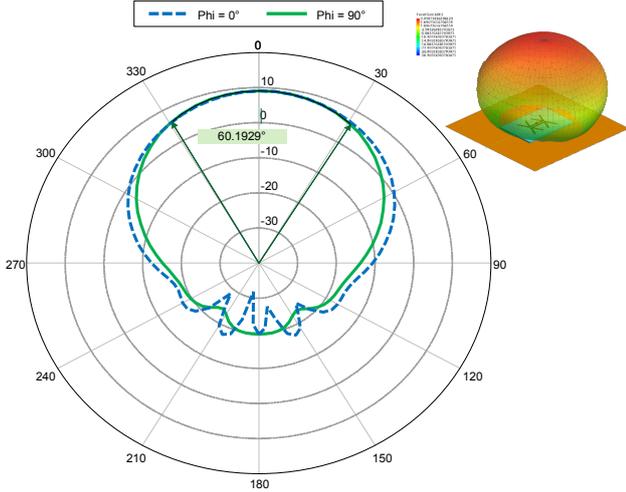


Fig. 3. Simulated (FEKO) gain of the CDAA (xoz-plane: $\phi=0^\circ$ and yoz-plane: $\phi=90^\circ$) at the frequency $f=17.7$ GHz.

The gain in the direction of Oz-axis as a function of the frequency is depicted in Fig. 4. This gain is higher than 8.5 dBi in the analyzed frequency band (17 GHz - 24 GHz) and reaches its maximum (11 dBi) at 22.3 GHz.

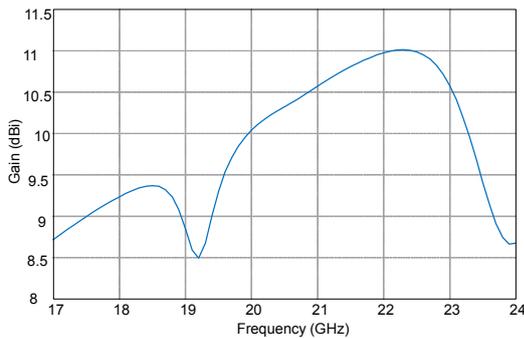


Fig. 4. Simulated gain (FEKO) along Oz-axis versus frequency.

The simulated input impedance viewed by the diode port of the CDAA (rectenna) is depicted in the Fig. 5. This impedance (Z_{in_a}) should be the conjugate of the input impedance of the rectifier (Z_{in_r}) in order to achieve the maximum power transfer between CDAA and the rectifier.

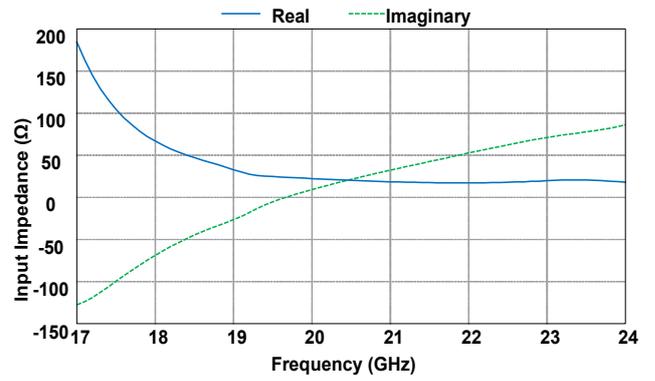


Fig. 5. Simulated input impedance (FEKO) of the CDAA (rectenna) structure viewed by diode port.

C. Design Methodology

A low-cost silicon Schottky diode (SMS201 from Aeroflex Metelics) in a molded plastic (DFN) package was adopted for this design. This diode is designed for a broadband zero bias detector. It can be used below 26.5 GHz for power detection up to 10 dBm. Most of previously reported designs below 10 GHz uses GaAs Schottky diodes with an higher cut-off frequency (e.g. M/A Com MA4E-1317 diode used in [1]-[2],[6],[8] has a cut-of frequency of 70 GHz while the Metelics MZBD-9161 diode used in [1]-[3] has a cut-off frequency of 110 GHz). This low-cost silicon diode has also a good thermal behavior and can be mounted using a classical soldering process at high temperature (260°C) while the above-mentioned GaAs diodes require a more sensitive mounting process at a lower temperature ($< 150^\circ\text{C}$).

The proposed design methodology combines full-wave electromagnetic simulation (for CDAA) and non-linear circuit simulation (for rectifier part). The main goal is to simultaneously achieve a high gain for the CDAA with an acceptable matching in a large bandwidth between CDAA and the rectifier for various load impedance and incident RF power. This was performed for maximizing the overall conversion efficiency of the rectenna. The design procedure consists of the following steps:

(i) the CDAA was first designed, simulated and optimized using FEKO software for operation in the desired frequency band. The diode was replaced by a voltage port while the capacitance (1.5 pF) and the load (e.g. 300 Ω) were modeled as port loads. The antenna radiation pattern and the distribution of the electric currents on the metallic strips were simulated in order to insure the proper radiation mechanism and to maximize the gain in the Oz-axis direction (that is, in the direction perpendicular to the rectenna surface);

(ii) the input impedance (Z_{in_a}) as well as the gain along the Oz-axis ($\theta=0^\circ$; $\phi=0^\circ$) of the CDAA (simulation model shown in Fig. 1) was computed in the targeted frequency band as a function of the diode/capacitance mounting position;

(iii) non-linear (harmonic balance) simulation was performed by using the AWR software for the rectifier part of the rectenna. The coplanar stripline sections supporting the rectifier were modeled as a sub-circuit at the electromagnetic level because of the lack of an appropriate transmission line model in AWR. A generic non-physical diode model available in AWR was customized with the SMS201 diode parameters [10]. The input impedance (Z_{in_r}) of the rectifier was determined as a function of the diode/capacitance mounting position;

(iv) the position the capacitance was adjusted to ensure the matching condition, that is $Z_{in_a}=Z_{in_r}^*$ where the asterisk denotes the complex conjugated.

III. RECTENNA: EXPERIMENTAL RESULTS

The rectenna was fabricated on Rogers 6002 substrate (relative permittivity: 2.94, loss tangent: 0.0012, thickness: 508 μm). The layout was accommodated with the manufacturing tolerances available in a university unit equipped for general (low frequency) PCB manufacturing. A metallic plane (size: $\approx 4 \text{ cm} \times 4 \text{ cm}$) was positioned at 1.2mm below the rectenna. A 1.2 mm thick Rohacell HF51 [13] (dielectric constant in the range of 1.08) layer was used as spacer between rectenna and its reflector. The main dimensions of the manufactured rectenna (see Fig. 1 and Fig. 2) are: $l_x=1.4\text{mm}$, $l_y=1.8\text{mm}$, $l_{ay}=10\text{mm}$, $l_{ax}=8.3\text{mm}$, $l_{ad}=4.3\text{mm}$, $l=7 \text{ mm}$ and $l_c=6 \text{ mm}$.

A. Experimental Setup

An experimental setup (shown in Fig. 6) was used in order to recreate the electromagnetic environment existing on antenna panels of the broadcasting satellites. A microwave signal generated from an Anritsu MG3694B generator was injected at the input of a horn antenna (VT220HA20-SK from Vector Telecom) which illuminated the rectenna under test with a linear polarized E-field.

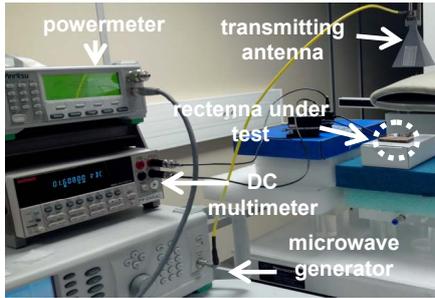


Fig. 6. Experimental setup used for characterizing the performances of manufactured rectennas .

The power density ($\mu\text{W}/\text{cm}^2$) can be computed as a function of E-field effective value E (V/m) on the antenna surface or as a function of the RF power P_t delivered to the transmitting horn antenna of gain G_t and positioned at the distance d from the rectenna under test, as follows:

$$S = \frac{E^2}{120 \cdot \pi} \cdot 100 = \frac{30 \cdot P_t \cdot G_t}{d^2 \cdot 120 \cdot \pi} \cdot 100 \quad (2)$$

An automatic acquisition routine was implemented in Labview software from National Instruments to speed-up the acquisition process. The harvested DC voltage was measured with the microwave source OFF (DC noise: V_{off}) and with microwave source ON (DC voltage: V_{on}). The DC voltage of interest (V_{DC}) was then derived from the difference $V_{\text{DC}}=V_{\text{on}} - V_{\text{off}}$. The DC power can be computed from the measured DC voltage as long as the load is known. The measured loss due to the coaxial cable and connectors between antenna and the signal generator was in the range of 2.5 dB in the operating frequency band.

B. Simulation and Experimental Results

The frequency of 17.7 GHz was targeted by our design because available cartographic maps (for the expected E-field values on the K band antenna panels) indicated that an incident E-field up to 127 V/m may illuminate few regions of these panels [2]. Nevertheless the adopted ‘non-resonant’ matching technique (between CDAA and the rectifier) can provide a wideband behavior to the rectenna. The simulated non-linear input impedance (obtained by using AWR software and harmonic balance technique) of the rectifier is represented in the Fig. 7. The estimated RF power of the input at $f=17.7$ GHz of rectifier by using Friis free-space formula [12] (with injected power into the transmitting antenna: $P_t=23.5 \text{ dBm}$, measured gain of the transmitting horn antenna: $G_t \approx 18 \text{ dBi}$, receiving CDAA simulated gain antenna $G_r \approx 9 \text{ dBi}$, distance between the input port of the transmitting and the receiving antenna $d=25 \text{ cm}$) is in the range of 5 dBm. The simulated (AWR) nonlinear input impedance of the rectifier derived from Fig. 7 is $Z_{in_r} \approx (76+42*j)\Omega$ while the simulated (FEKO) input impedance of the CDAA (Fig. 5) is $Z_{in_a} \approx (86-87*j)\Omega$.

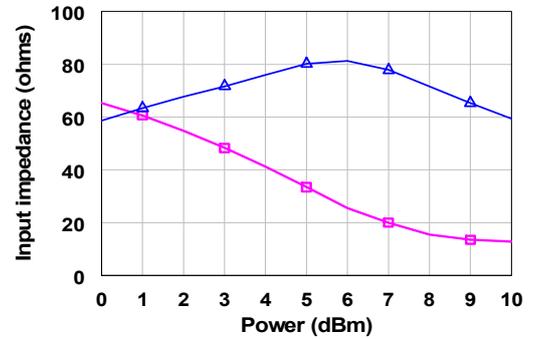


Fig. 7. Simulated (AWR) nonlinear input impedance (triangle marker: real part; square marker: imaginary part) of the rectifier as function of the RF power at the rectifier input (frequency $f=17.7$ GHz).

The measured DC power at the input of an (optimal) resistive load of 300 Ω by using the setup described in the section II. A (Fig. 6) is shown in the Fig. 8. These measurement results were obtained at $f=17.7$ GHz. The delivered power of the Anritsu MG3694B signal generator was $P=25 \text{ dBm}$ while the injected power on the coaxial input port of the transmitting horn antenna was in the range of 22.5 dBm.

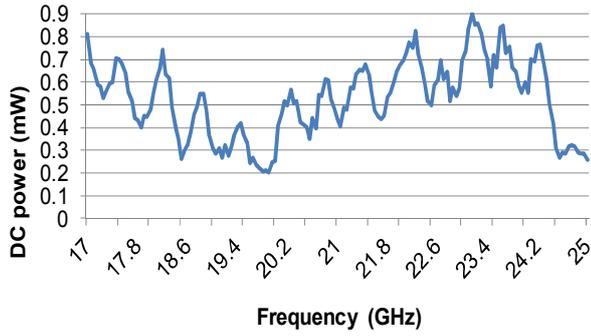


Fig. 8. Simulated (AWR) nonlinear input impedance (triangle marker: real part; square marker: imaginary part) of the rectifier as function of the RF power at the rectifier input (frequency $f=17.7$ GHz).

Due to the inherent limitations of the adopted design methodology and simulation models the obtained DC power at 17.7 is 0.52 mW. A maximum DC power of 0.9 mW on a $0.3\text{k}\Omega$ load was obtained at $f=23.15$ GHz. At this frequency the gain of CDAA along the Oz-axis reaches a maximum value of 11 dBi while the simulated gain (along the Oz-axis) is 9 dBi at 17.7 GHz. The efficiency η (in %) of a rectenna can be computed by using the following worst-case definition [13]:

$$\eta = \frac{P_{DC}}{S \cdot A_G} \cdot 100 \quad (3)$$

where P_{DC} is the harvested DC power, S is the incident electromagnetic power density and A_G (in cm^2) denotes the area of the radiating surface. Here $P_r \approx 22.5$ dBm, $G \approx 18$ dBi and $d=25$ cm. Thus $E \approx 73$ V/m and $S \approx 1413$ $\mu\text{W}/\text{cm}^2$. By taken into account the overall area of the rectenna $A_G \approx 2.5$ cm^2 a worst-case conversion efficiency around of 15% at 17.7 GHz (harvested DC power: 0.9 mW) and 25 % at 23.15 GHz (harvested DC power: 0.9 mW) were obtained.

The proposed design methodology suffers from several limitations: (i) the impact of the nonlinear behavior of the diode on antenna is neglected during CDAA design as explained in section II.B; (ii) the impact on rectenna performances of the diode package and of the two via holes used to connect the diode (mounted on the top side of the rectenna) with the low-pass filtering section of the rectifier (located on the bottom side) is not taken into account in the AWR model; (iii) the simulation model adopted for the diode could be improved because key parameters of the diode (series resistance, ideality factor, etc.) are not provided in the datasheet [10]. Also, the PCB was fabricated in a university unit equipped for standard (low frequency) PCB low-cost manufacturing. The capacitance mounting position is critical and the exact mounting position is not well controlled by the adopted manual assembly process (tolerances in the range of 0.5 mm). Works are under run in order to overcome these limitations and to improve the global rectenna efficiency.

IV. CONCLUSION

An innovative rectenna topology and the adopted design methodology for K band application were presented. With only one low-cost silicon Schottky diode and using this ultra-compact rectenna ($2.5 \text{ cm}^2/1.5$ square wavelengths at 23 GHz) a DC power up to 0.9 mW can be harvested for an incident E-field close to 73 V/m in the K band.

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